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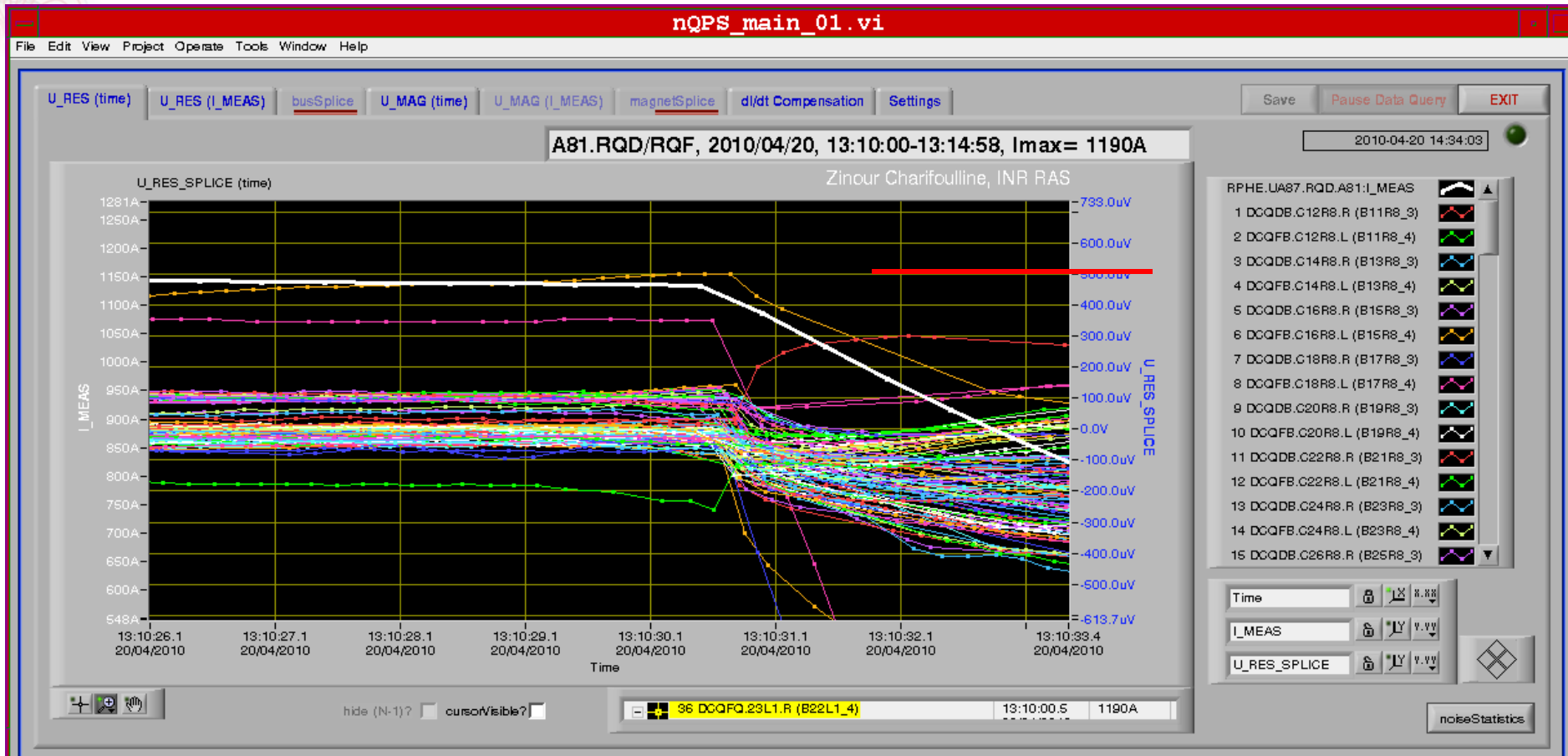
# MP3 report - week 17

## 19/4/2010 - 25/04/2010

MP3/QPS team in CCC

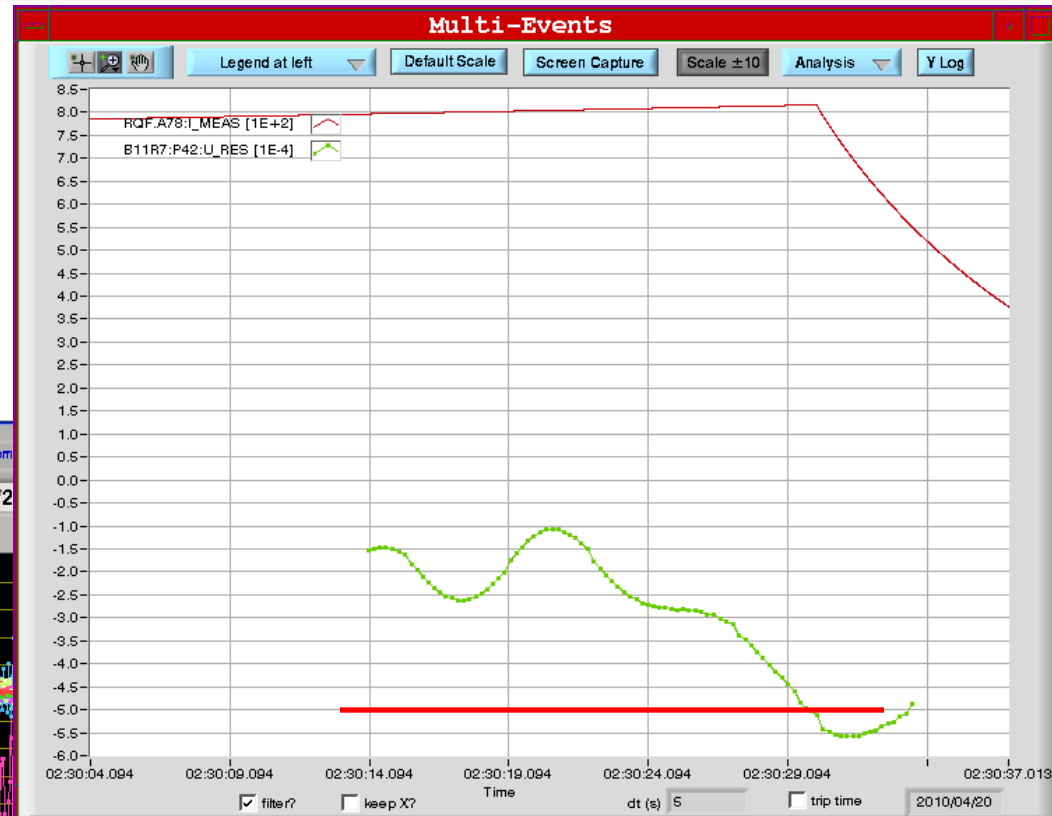
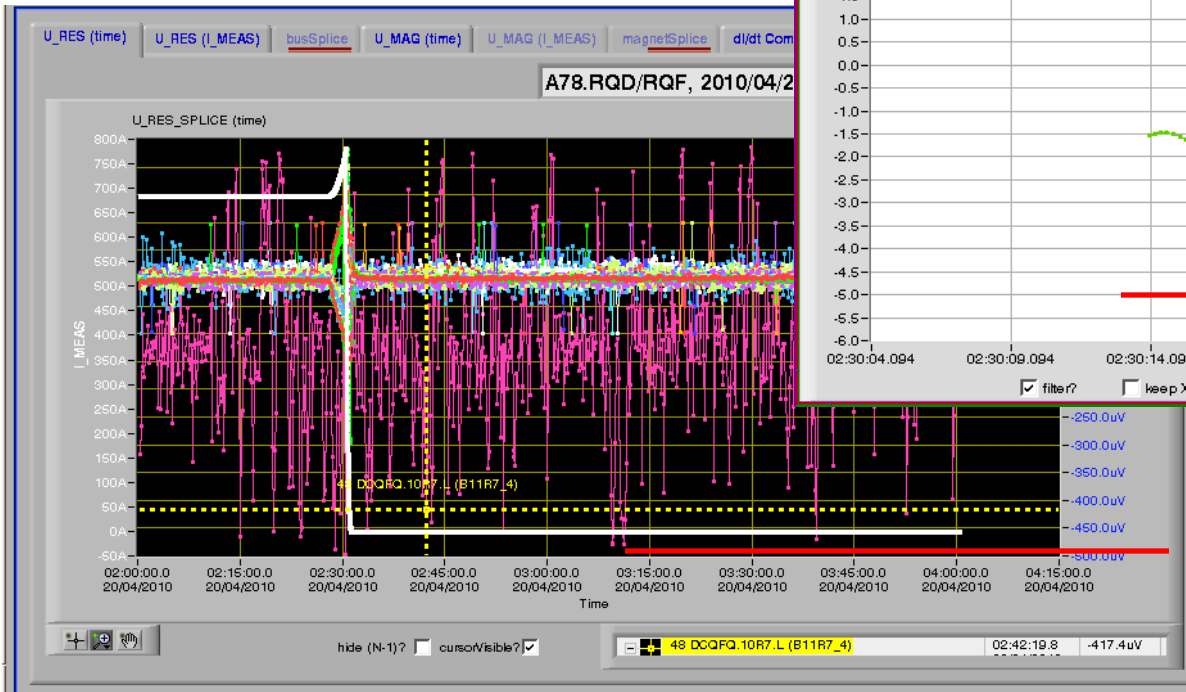
# RQD/F.A81 on Tuesday and Friday

- Trip induced by nQPS at 6kA on Tuesday, at 1.4kA on Friday
- Noisy board in B22L1 : New Threshold of 0.6mV



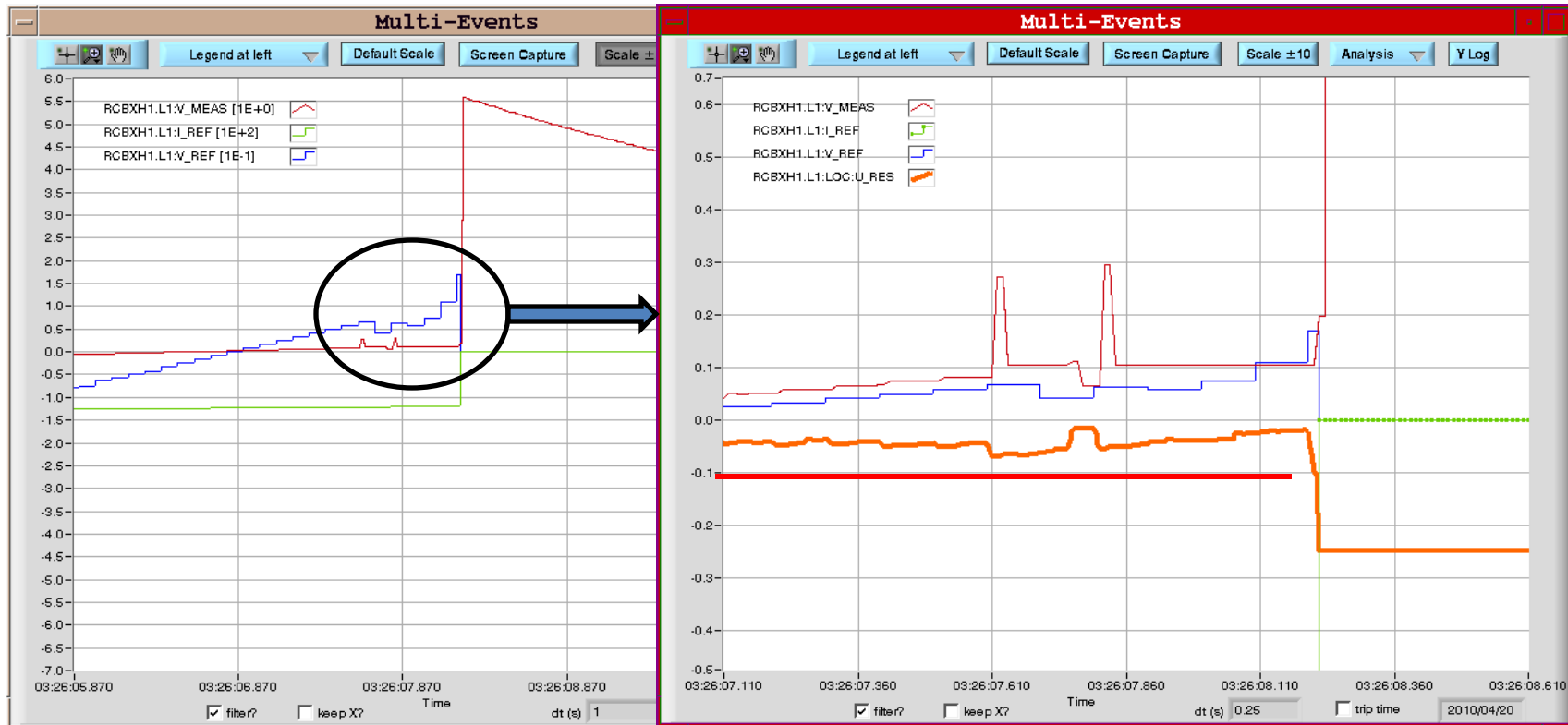
# RQF.A78 on Tuesday

- Trip induced by nQPS at 781A
- Noisy board in B11R7 :  
new Threshold of 0.6mV



# RCBXH1.L1

- Tripped during ramp down of a precycle at 120A.
- 0 Voltage crossing : Perturbation created by PC when the voltage polarity is changing can be not compensated by QPS





# RQTD/F.A34B2 - RQS.A78B2

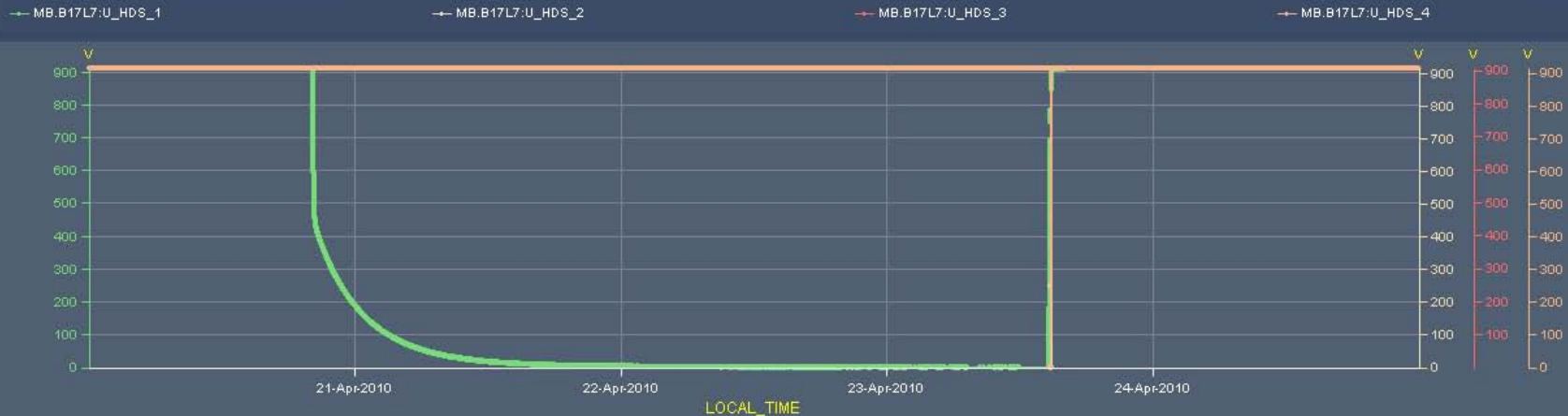
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- RQTD/F.A34B2 tripped at 3.5TeV due to the trim function  
Hope to fix it with new protection function : Time discrimination will be increased from 20ms to 200ms  
( planned in the next technical stop)
  
- RQS.A78B2 tripped on Wednesday : bad trim function incorporated

# Quench heater power supply in RB.A67

- 1 QH power supply discharged (MB.B17L7) from Tuesday  
Can be tolerated temporarily @ 3.5 TeV
- Fixed the 23/04 during intervention for UPS in RE73.

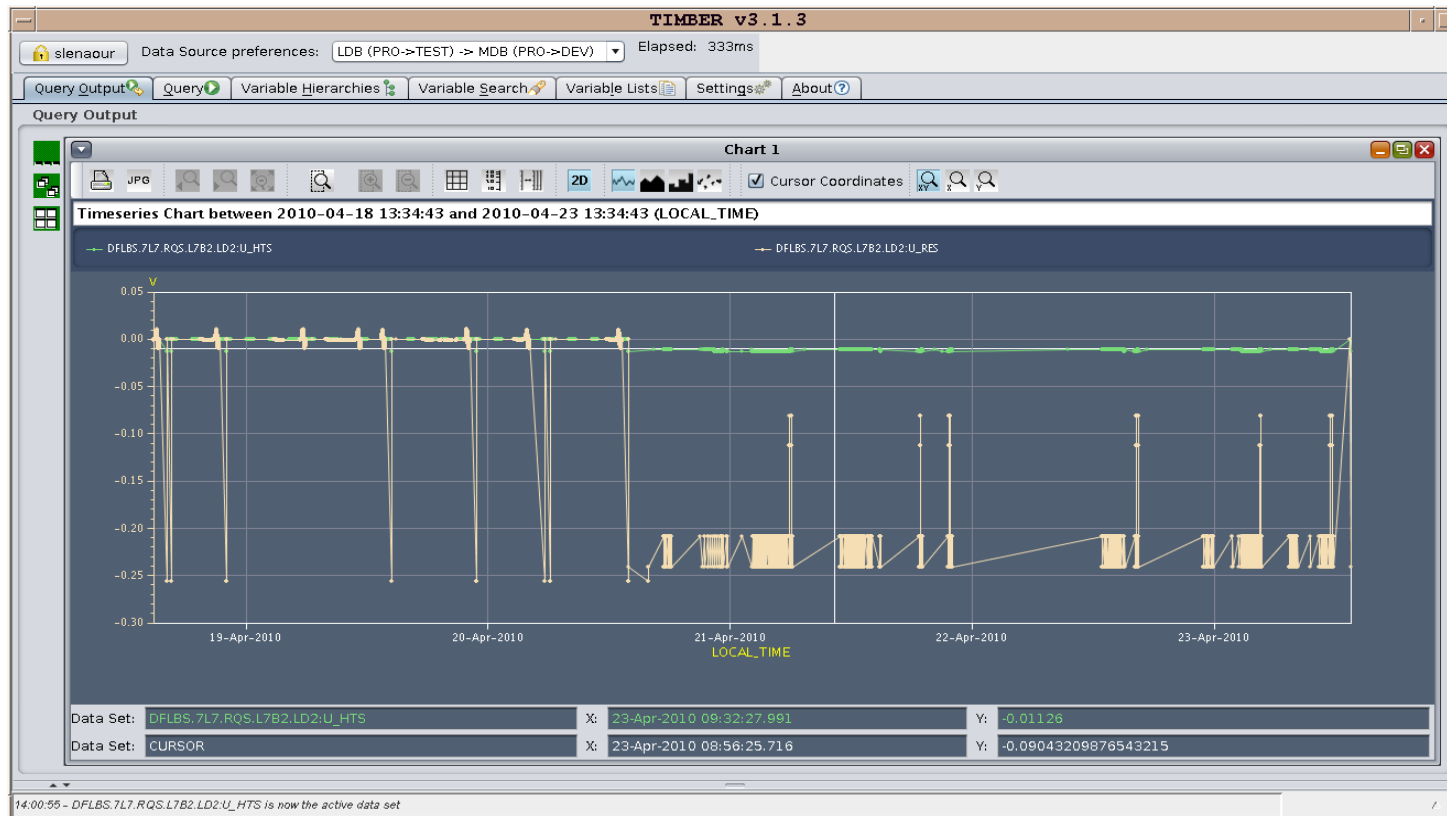
Timeseries Chart between 2010-04-20 00:00:00 and 2010-04-25 00:00:00 (LOCAL\_TIME)



# RQS.L7B2 on Friday and Sunday

QPS not OK on 600A global detector. 1 board on current lead blocked since 3days. Not a major issue because the redundant board was still working.

*QPS\_OK of 600A systems are not checked during the software interlock*



Similar problem showed up on Sunday . Temporally fixed

# RB.A81 on Sunday

- Quench loop controller in point 8 (even side) with wrong values (correct values : I\_loop ≈ 50mA, V\_loop ≈ 45V) and some bad status .
- Not sure that communication with PIC or PC was correct -> intervention required

1 - RQD.A81.EVEN DQQLC type EVEN side RB, RQF

Status Trend RQD.A81.EVEN

CFC\_SR8\_DT8FA\_IP8\_DT8A DATA Connection OK

CIRCUIT		DQQLC	
ST_PWR_PERM	ST_CIRCUIT_OK_QPS	I_LOOP	-0.282 A
Last Circuit bad at (UTC)	ST_CMD_DISCHARGE_QPS	U_LOOP	-119.30 V
1972.08.28 03:34:16.280	ST_CMD_SELECTED_HEATER		

ST\_CUR\_SRC  
ST\_CUR\_SEN  
ST\_EARTH\_FLT  
ST\_COHER\_OK  
ST\_PWR\_PERM  
ST\_PWR\_OK

DQAMG Status

Logging\_ON Logging\_Data

DQQLC EVEN Command

Select

• Even side

1 - RB.A81.ODD DQQLC type ODD side RB for circ

Status Trend RB.A81.ODD

CFC\_SR1\_DT1FA\_IP1\_DT1F DATA Connection OK

CIRCUIT		DQQLC	
ST_PWR_PERM	ST_CIRCUIT_OK_QPS	I_LOOP	-0.038 A
Last Circuit bad at (UTC)	ST_CMD_DISCHARGE_QPS	U_LOOP	11.90 V
1974.09.16 13:38:08.281	ST_CMD_SELECTED_HEATER		

ST\_CUR\_SRC  
ST\_CUR\_SEN  
ST\_EARTH\_FLT  
ST\_COHER\_OK  
ST\_PWR\_PERM  
ST\_PWR\_OK

DQAMG Status

Logging\_ON Logging\_Data

DQQLC ODD Command

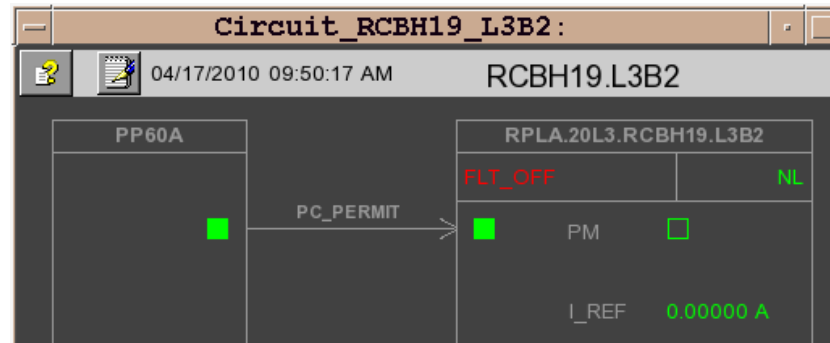
Select

Odd side



# Other MP3 issues since 12<sup>th</sup> of April 2010

- RCBH19.L3B2 & RCBV19.L3B1 : Seems to have a PC issue  
EPC informed



# MP3 issues since April 19<sup>th</sup>

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- Some trips of circuits that were repowered after analysis:
  - RCBCH5.R5B1(19/04)
  - RCBXH1.L1(20/4); RCBXH2.L8 (23/4)
  - RQTD/F.A34B2; RQS.A78B2
  - RQD/F.A81 (20/4 and 23/4); RQD/F.A78 (20/4)
    - Threshold was increased to 600uV on B11R7 and B22L1, until next technical stop
  - RB.A81
- Lost of Communication on RCBV25.R7B2